

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Antonio L. P. Rotondaro

Docket No: TI-31133

Serial No: 10/001,483

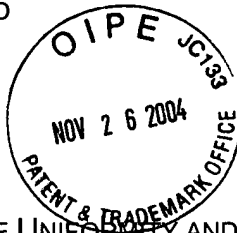
Conf. No: 1903

Examiner: Michelle Estrada

Art Unit: 2823

Filed: 11/01/2001

For: METHOD TO IMPROVE THE UNIFORMITY AND REDUCE THE SURFACE ROUGHNESS  
OF THE SILICON DIELECTRIC INTERFACE



EXTENSION OF TIME

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-23-04.

  
Ann Trent

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicant respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above identified Application.

The fee for this extension is indicated below:

X One Month (\$110)

\_\_\_ Three Months (\$980)

\_\_\_ Two Months (\$430)

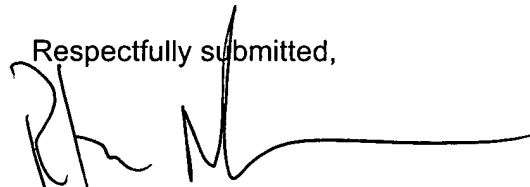
\_\_\_ Four Months (\$1,530)

Any further necessary extension of time is hereby requested. Charge any and all fees, or credit any overpayment, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

11/30/2004 HBERHE 00000059 200668 10001483  
01 FC:1251 110.00 DA

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-4258

Respectfully submitted,

  
Peter K. McLarty  
Attorney for Applicant  
Reg. No. 44,923